

AMENDMENTS TO THE SPECIFICATION

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Please amend the paragraph bridging pages 3 and 4, from line 14 on page 3 through line 3 on page 4, as follows:

B1  
Exemplar of the art are U.S. Patent 6,073,255 for *Method of Reading System Log* issued to Nouri et al., U.S. Patent 6,035,355 for *PCI System and Adapter Requirements Following Reset* issued to Kelley et al., U.S. Patent 6,070,255 for *Error Protection Power-on-self-test for Memory Cards having ECC on Board* issued to Dell et al., U.S. Patent 5,860,001 for *Computer System having at Least Two Boot Sequences* issued to Cromer et al., U.S. Patent 5,978,913 for *Computer with Periodic Full Power-on Self Test* issued to Broyles et al., U.S. Patent 6,052,800 for *Method and System for Updating Information on an Intelligent Display Device Monitoring a Computer System* issued to Gentile et al., U.S. Patent 5,742,616 for *System and Method Testing Computer Memories* issued to Torreiter et al. disclose methods and devices to perform power-on self-tests. We have noticed that the art does not show a device that displays the POST states in an easily manageable fashion.